

## Partner search/offer

Date (19-01-24)

### (\*) – Mandatory

- (\*) Relevant topic(s) in work programme (code & name of the topic(s) line(s))

HORIZON-CL4-2024-DATA-01-01: AI-driven data operations and compliance technologies (AI, data and robotics partnership) (IA)

HORIZON-CL4-2024-DATA-01-03: Piloting emerging Smart IoT Platforms and decentralized intelligence (IA)

HORIZON-CL4-2024-DIGITAL-EMERGING-01-03: Novel paradigms and approaches, towards AI-powered robots– step change in functionality (AI, data and robotics partnership) (RIA)

HORIZON-CL4-2024-DIGITAL-EMERGING-01-04: Industrial leadership in AI, Data and Robotics boosting competitiveness and the green transition (AI Data and Robotics Partnership)

HORIZON-CL4-2024-HUMAN-03-01: Advancing Large AI Models: Integration of New Data Modalities and Expansion of Capabilities (AI, Data and Robotics Partnership) (RIA)

HORIZON-CL4-2024-HUMAN-03-02: Explainable and Robust AI (AI Data and Robotics Partnership) (RIA)

- Quick description of the project concept

Describe the

- objectives,
- activities,
- type of partners already involved and their skills
- partners requested and their skills

- (\*) Description of the expertise requested/proposed (up to 1000 characters)

Expertise proposed: AI chip/chiplet design, AI-enabled ASIC solutions, semiconductor IP design (neural processor subsystem), memory expertise (in-memory computing, non-volatile technologies), custom AI processor. AI technics for embedded applications (IoT, edge AI), software development (modeling, quantization, firmware/compiler level), hardware-constrained devices, deep learning, CNN, transformers, LLM, generative AI.

Expertise requested: modeling/data science, board design, AI-powered embedded systems development, embedded AI application development (wearables, IoT, drones and robotics, cameras, automotive, datacenters).

- (\*) Keywords describing the expertise requested/proposed (up to 10 words)

Proposed: AI chip/chiplet/IP design, AI software for embedded, deep learning, computer vision, LLM, generative AI

Requested: data science, AI models, board design, AI for embedded systems, AI applications

### Organisation information

<p><b>(*) Organisation and country:</b> Neurxcore, France</p>
<p><b>(*) Type of organisation:</b>  <input type="checkbox"/> Enterprise <input checked="" type="checkbox"/> SME <input type="checkbox"/> Academic <input type="checkbox"/> Research institute <input type="checkbox"/> Public Body <input type="checkbox"/> Other: Association</p>
<p><b>Former participation in FP European projects?</b>  <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
<p><b>Web address:</b>  <a href="https://neurxcore.com/">https://neurxcore.com/</a></p>
<p><b>Description of the organisation:</b>  Neurxcore is a fabless semiconductor company developing AI processors (neural processors for deep learning) combining digital and/or in-memory computing (SRAM/Flash) technics to achieve high-energy efficiency and high throughput. These neural processors are highly configurable, tunable and can cover computer vision, natural language processing (LLM) and generative AI applications from IoT/sensors to datacenter.</p>

**(\*) Contact details**

<b>Contact person name</b>	Virgile JAVERLIAC
<b>Telephone</b>	+33608038738
<b>E-mail</b>	virgile.javerliac@neurxcore.com
<b>Country</b>	France